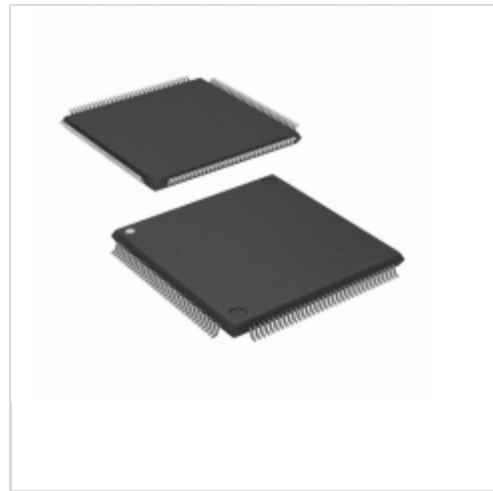


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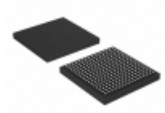
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Specifications

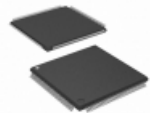
Voltage - Supply:	3 V ~ 3.6 V
Supplier Device Package:	144-LQFP (20x20)
Speed:	16MHz
Series:	FPLSIC®
Program SRAM Bytes:	20K-32K
Packaging:	Tray
Package / Case:	144-LQFP
Operating Temperature:	-40°C ~ 85°C
Mounting Type:	Surface Mount
Interface:	I ² C, UART
FPGA SRAM:	18kb
FPGA Registers:	2862
FPGA Gates:	40K
FPGA Core Cells:	2304
EEPROM Size:	1M x 8
Data SRAM Bytes:	4K ~ 16K
Core Type:	8-bit AVR

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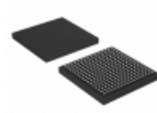
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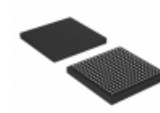
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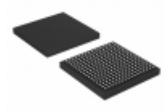
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 Kbytes of SRAM and On-chip P

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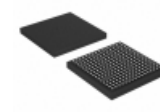
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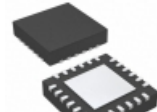
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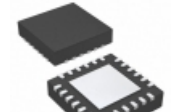
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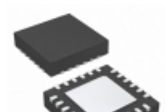
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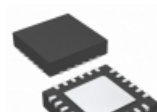
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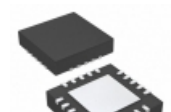
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